CONCRETE: Concrete Operates on Ciphertexts Rapidly by Extending TFHE

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1 INTRODUCTION

The idea of fully homomorphic encryption (FHE) emerged in 1978 when Rivest, Adleman and Dertouzos started talking about privacy homomorphisms [9]. However, no solution was found until 2009 when Gentry presented the first instantiation of a FHE scheme [5].

A common feature in Gentry’s original cryptosystem and in all subsequent FHE schemes is that ciphertexts contain noise. The vast majority of homomorphic operations make this noise grow. If not controlled, the noise in a ciphertext can compromise the encrypted plaintext and induce incorrect results at decryption time. This fact inherently limits the number of operations that can be performed on ciphertexts. A homomorphic encryption scheme supporting a predetermined noise threshold is termed leveled. The groundbreaking idea of [5] which made FHE possible, is a technique called bootstrapping enabling to reduce the noise—using only public material—when needed. This way, there is no more limitation on the maximal number of operations that can be performed and the scheme becomes fully homomorphic.

Since Gentry’s FHE scheme, many other schemes have been presented in the literature, but the bootstrapping technique has remained the main bottleneck, both in terms of running time (several minutes) and of key sizes (gigabytes). This state of affairs dramatically changed in 2015 when Ducas and Micciancio introduced the FHEW cryptosystem [3] mastering a bootstrapped NAND gate in less than a second yet with keys of about 1 GB in size. This was further improved in 2016 by Chillotti et al. and gave rise to the TFHE cryptosystem [2] running any bootstrapped binary gate in a few tens of milliseconds and with keys in the order of megabytes.

Furthermore, in addition of being relatively fast, the bootstrapping procedure in TFHE can be ‘programmed’: it enables the homomorphic evaluation of any function on a ciphertext while reducing the noise. This powerful technique opens new application scenarios for the practical use of FHE technologies.

The above reasons explain our choice to implement and extend the TFHE scheme in our CONCRETE library (CONCRETE stands for “Concrete Operates on Ciphertexts Rapidly by Extending TFHE”).

As for the programming language, we picked Rust, a new language that has become popular thanks to its performance (as good as C or C++ with the advantages of a high-level programming language), its memory safety, and its efficient memory usage and access.

CONCRETE follows TFHE’s implementation: it makes use of arithmetic modulo q with a small modulus natural for a machine such as $q = 2^{32}$ or $q = 2^{64}$. This is very convenient because these moduli are natively supported while using unsigned integers with 32 or 64 bits and their respective efficient addition and multiplication operations. Further, CONCRETE unleashes the full potential of TFHE by encrypting and bootstrapping values of arbitrary format, including real numbers. In particular, it is not limited to boolean computation. This is achieved through the use of encoding functions.

The library is designed in layers. The lower-level layer, called Core API, is meant to be accessible by FHE experts and aims to be as efficient as possible. The layer above, named Crypto API, wraps the operator layer in a user-friendly way. It is accessible and easy to use by any programmer (even with a limited understanding of the cryptography behind it). It offers in addition extra capabilities, including automated metadata keeping track of the amount of noise.

2 CRYPTOGRAPHIC CONTENT OF THE LIBRARY

TFHE [2] is based on the learning with errors (LWE) problem [8], a hard problem on lattices, and its variant Ring-LWE [7, 10]. It deals with two types of ciphertexts: LWE ciphertexts and RLWE ciphertexts.

2.1 Encryption & Decryption

LWE ciphertexts. Let $\mathbb{B}$ denote the set $\{0, 1\}$ and let $q$ be a power of two. Let also $\mathbb{Z}_q = \mathbb{Z}/q\mathbb{Z}$. The plaintext space is $\mathbb{Z}_q$. An LWE ciphertext $c \leftarrow \text{LWE}_\mu(\mu)$ encrypting a plaintext $\mu \in \mathbb{Z}_q$ under the secret key $s = (s_1, \ldots, s_n) \in \mathbb{B}^n$ is a tuple

$$c = (a_1, \ldots, a_n, b) \in (\mathbb{Z}_q)^{n+1}$$

where $a_1, \ldots, a_n$ are sampled uniformly at random in $\mathbb{Z}_q$ and $b = \sum_{j=1}^n s_j a_j + \mu + e \pmod{q}$, with $e$ a (small) discretized Gaussian noise.

Given a ciphertext $(a_1, \ldots, a_n, b)$, the decryption algorithm uses the secret key $(s_1, \ldots, s_n)$ to obtain $\mu^* := b - \sum_{j=1}^n s_j a_j \pmod{q}$; see Fig. 1.3. If the message is encoded in the most significant bits (and thus if $\mu$ has its least significant bits set to zero) then, provided that the noise $e$ is not too large, rounding $\mu^*$ enables to recover the original $\mu$.

RLWE ciphertexts. Let $N$ be a power of two so that $X^N + 1$ is the $2N$th cyclotomic polynomial. Let also $\mathbb{B}_N[X] = \mathbb{B}[X]/(X^N + 1)$
and $\mathbb{Z}_{q,N}[X] = \mathbb{Z}_q[X]/(X^N + 1)$. The plaintext space for RLWE encryption is $\mathbb{Z}_{q,N}[X]$. An RLWE ciphertext $c \leftarrow \text{RLWE}_\mathbb{Z}(\mu(X))$, encrypting a plaintext $\mu(X) \in \mathbb{Z}_{q,N}[X]$ under the secret key $\mathbf{a} = (a_1(X), \ldots, a_k(X)) \in (\mathbb{Z}_N[X])^k$ is a tuple

$$c = (o_1(X), \ldots, o_k(X), b(X)) \in (\mathbb{Z}_{q,N}[X])^{k+1}$$

of $k + 1$ polynomials, where $o_1(X), \ldots, o_k(X)$ are sampled uniformly at random in $\mathbb{Z}_{q,N}[X]$ and $b(X) = \sum_{j=1}^{k} a_j(X) o_j(X) + \mu(X) + \epsilon(X) \pmod{(q, X^N + 1)}$, with $\epsilon(X)$ a polynomial with (small) discretized Gaussian coefficients.

Given a ciphertext $(o_1(X), \ldots, o_k(X), b(X))$, the decryption proceeds by computing (in $\mathbb{Z}_{q,N}[X]$) the polynomial $\mu^*(X) := b(X) - \sum_{j=1}^{k} a_j(X) o_j(X) = \mu(X) + \epsilon(X)$. As for LWE ciphertexts, if the message is encoded in the most significant bits of the coefficients of $\mu^*(X)$, rounding each coefficient of $\mu^*(X)$ yields back the original $\mu(X)$, provided that the noise $\epsilon(X)$ is not too large.

### 2.2 Real Encoding on $[a, b]$.

The plaintext space is defined over $\mathbb{Z}_q$. This section describes how to represent messages of a real domain $\mathcal{D} = [a, b] \subset \mathbb{R}$ with a subset of $\mathbb{Z}_q$. To do so, a number of bits $n_{\text{msg}}$ are reserved to store the message $m \in \mathcal{D}$ as a number of bits $n_{\text{pad}}$ in the MSBs. There are two different settings for the encoding: (i) the number of precision bits contained in the plaintext is maximized (Fig. 1.4) and (ii) the precision is limited to $n_{\text{msg}}$ bits (Fig. 1.1).

The encoding functions are defined as (i) $\text{Encode}(m \in \mathcal{D}) = \lfloor q \cdot (m - a)/(b - a) \cdot 2^{n_{\text{msg}}}/\delta \rfloor$ and as (ii) $\text{Encode}(m \in \mathcal{D}) = \lfloor q \cdot (m - a)/(b - a) \cdot 2^{n_{\text{msg}}}/\delta \rfloor$, with $\delta = b - a + \sigma$ and $\sigma = (b - a)/(2^{n_{\text{pad}}} - 1)$. The quantity $\sigma$ is a security margin to prevent wrong decryption nearby $m \in Z_q$ (i.e., $a$ and $b$ in $\mathbb{D}$), which boils down to actually consider the interval $[a, b + \sigma]$.

The decoding functions of a noisy plaintext $\mu' \in Z_q$ are defined as (i) rounding $\mu'$ to remove the noise and get $\mu$, and then computing $\text{Decode}(\mu) = a + (\mu \cdot 2^{n_{\text{pad}}}/q) \in \mathcal{D}$, and (ii) simply computing $\text{Decode}(\mu') = a + (\mu' \cdot 2^{n_{\text{pad}}}/q) \in \mathcal{D}$.

### 2.3 Addition & Scalar Multiplication

LWE ciphertexts can readily be added together (component-wise addition). The result of an addition is an LWE ciphertext in $(\mathbb{Z}_q)^{n+1}$ encrypting the sum (modulo $q$) of the input plaintexts.

This works analogously with RLWE ciphertexts. Note that in both cases the noises also add up as a result of the addition.

**Remark 1.** A way to avoid the modular reduction in the sum of the plaintexts is to make sure that they are small enough. For example, suppose that $q = 2^{32}$ and that plaintexts viewed as 32-bit integers are of the form $\{x \cdot 2^{25}, 0 \leq x < 2^5\}$. This means that plaintexts are made of 2 bits set to zero (i.e., the 2 left-most significant bits—theese bits are called padding bits), 5 bits containing the message, and 25 bits set to zero; see Fig. 1.1. Hence, as illustrated in Fig. 1.2, the addition of two such plaintexts yields a plaintext as an element of $\{x \cdot 2^{25}, 0 \leq x < 2^{25}\}$, that is, with only one bit of padding left.

By extension, LWE (resp. RLWE) ciphertexts can be multiplied by an integer constant. The noise after the scalar multiplication grows proportionally with respect to the integer constant.

### 2.4 External Product

LWE and RLWE ciphertexts can be added; unfortunately, they cannot be multiplied as easily. One may resort to a matrix-based approach [6] to this end. This involves two parameters: a basis $\mathcal{B} \in \mathbb{N}$ and a number of levels $t \in \mathbb{N}$. To avoid rounding, it is assumed that $\mathcal{B}^t \mid q$.

**RGSW ciphertexts.** An RGSW ciphertext of $\mu(X) \in \mathbb{Z}_N[X]$ under secret key $\mathbf{a} = (a_1(X), \ldots, a_k(X)) \in (\mathbb{Z}_N[X])^k$ is a matrix $\mathbf{G} \in (\mathbb{Z}_{q,N}[X])^{(k+1)\times(k+1)}$ whose row $\#_t$ where $t = i + (j - 1)t$, for $1 \leq i \leq t$ and $1 \leq j \leq k + 1$, is an RLWE encryption under key $\mathbf{a}$ of plaintext $\mu(X) \equiv -a_j(X) \mu(X) \pmod{q}$, with $a_{k+1}(X) \equiv -1$. This is written $\mu(X) \leftarrow \text{RGSW}_\mathcal{B}(\mu(X))$.

**External product.** The external product [2, § 3.3] of an RGSW ciphertext $\mathbf{G}_1 \leftarrow \text{RGSW}_\mathcal{B}(\mu_1(X))$ and of an RLWE ciphertext $\mathbf{c}_2 \leftarrow \text{RLWE}_\mathcal{B}(\mu_2(X))$ is noted $\mathbf{c}_3 = \mathbf{G}_1 \square \mathbf{c}_2$. The resulting ciphertext $\mathbf{c}_3$ is an RLWE encryption of $\mu_1(X) \cdot \mu_2(X)$. The external product is defined by $\mathbf{G}_1 \square \mathbf{c}_2 = \mathbf{G}_1^{-1}(\mathbf{c}_2) \cdot \mathbf{c}_2$, where $\mathbf{G}^{-1}(\mathbf{c}_2)$ is the gadget decomposition of $\mathbf{c}_2$. The transformation $\mathbf{G}_1^{-1}(\cdot)$ flattens a vector of $(k + 1)$ polynomials of $\mathbb{Z}_{q,N}[X]$ into a row vector of $(k + 1)t$ polynomials of $\mathbb{Z}_N[X]$ with small coefficients in the range $\{-B/2, B/2\}$. The use of the gadget decomposition enables to control the noise growth in the external product.

**CMUX.** The main application of the external product in TFHE is the controlled multiplexer, or CMUX in short. Given two RLWE ciphertexts $\mathbf{c}_0 \leftarrow \text{RLWE}_\mathcal{B}(\mu_0(X))$ and $\mathbf{c}_1 \leftarrow \text{RLWE}_\mathcal{B}(\mu_1(X))$, the CMUX output acts as a selector to choose between $\mathbf{c}_0$ and $\mathbf{c}_1$ according to an RGSW encryption $\mathbf{G}_b \leftarrow \text{RGSW}_\mathcal{B}(b)$ of a control bit $b \in \mathbb{B}$. This can be computed through an external product as $\text{CMUX}(\mathbf{G}_b, \mathbf{c}_0, \mathbf{c}_1) = \mathbf{G}_b \square (\mathbf{c}_1 - \mathbf{c}_0) + \mathbf{c}_0$. The output is an RLWE encryption of $\mu_b(X)$.

### 2.5 Programmable Bootstrapping

As aforementioned, homomorphic operations may increase the noise in ciphertexts, and when the noise grows too much ciphertexts cannot be decrypted anymore. This is why, from time to time, it is
important to refresh noisy ciphertexts by reducing the amount of noise. Programmatic bootstraping (PBS) is a generalization of the bootstrapping technique allowing resetting the noise at a fixed level while at the same time homomorphically evaluating a function on the input ciphertext.

Programmable bootstrapping involves a series of three algorithms: (i) Switch Modulus, (ii) Blind Rotate, and (iii) Sample Extract. It is often followed by an LWE Key Switching algorithm. These algorithms are described below.

**Modulus switching.** The modulus switching is a classical technique in FHE for switching the modulus of ciphertexts. In TFHE, a modulus switching is performed prior to the blind rotation procedure. The Switch Modulus algorithm scales by $2N/q$ and rounds (i.e., performs the operation $\lceil 2N \cdot (\mod q)/q \rceil$) each component of the input LWE ciphertext (i.e., elements in $\mathbb{Z}_q$).

**Blind rotation.** The Blind Rotate algorithm homomorphically applies a rotation over the coefficients of the plaintext polynomial stored in an RLWE ciphertext. It is used to blindly put a desired coefficient in the constant term. This algorithm takes on input a (possibly trivial) $1$ RLWE ciphertext $c_T \in (\mathbb{Z}_q[NX])^{k+1}$ encrypting the test polynomial $T(X) = t_0 + t_1 X + \cdots + t_{N-1} X^{N-1}$, an LWE ciphertext $c = (a_1,\ldots,a_n,b) \in (\mathbb{Z}_qN)^{n+1}$ encrypting $\mu$, and $n$ RGSW ciphertexts $\{\psi_j\}_{1 \leq j \leq n}$ encrypting each bit of the secret key $(s_1,\ldots,s_n) \in \mathbb{B}^n$ used to encrypt $c$. Note that the RGSW ciphertexts and the RLWE ciphertext are encrypted with the same secret key. The noisy plaintext contained in $c$ is noted $\mu'$.

In Figure 2, the test polynomial is seen as a table where, for $0 \leq i \leq N-1$, $t[i]$ represents the $i$-th coefficient of $T(X)$. The resulting ciphertext is obtained by initiating an accumulator $ACC \leftarrow X^{-b} \cdot c_T$. Next, by using each ciphertext of $\{\psi_j\}_{1 \leq j \leq n}$ as control bit into a CMUX, the algorithm successively updates the accumulator as $ACC \leftarrow \text{CMUX}(\psi_j, ACC, X^{aj} \cdot ACC)$ for $j = 1,\ldots,n$. The output is an RLWE ciphertext encrypting the plaintext $X^{-b} + \sum_{\mu' = 1}^{n} s_j a_j \cdot \psi_T = X^{-\mu'} \cdot \psi_T$ which contains $t[\mu']$ in its constant coefficient.

**Sample extraction.** It is easy to extract any of the $N$ coefficients inside an RLWE plaintext $\mu(X) = \mu_0 + \mu_1 X + \cdots + \mu_{N-1} X^{N-1} \in \mathbb{Z}_q[NX]$ as an LWE ciphertext. The corresponding algorithm is called Sample Extract and simply consists in picking some of the coefficients of the input RLWE ciphertext and using them to build the output as an LWE encryption of the desired coefficient $\mu_i$. It is a public operation and adds no noise in the output.

**Key switching.** After the blind rotation and sample extraction, the resulting LWE ciphertext is encrypted with a secret key that is different from the one used on input. In order to revert to the original key, one needs to perform a key-switching operation (this operation is optional). As for the external product, the corresponding Key Switch algorithm involves two parameters: a basis $B_{\psi} \in \mathbb{N}$ and a number of levels $t_{\psi} \in \mathbb{N}$. The input dimension is denoted $n_\text{in}$ and the output dimension $n_\text{out}$. This procedure is classical in FHE and is possible thanks to the use of a key-switching key; i.e., a list of $n_\text{out}$ LWE ciphertexts encrypting each bit of the input secret key $\mu(X)$.

\footnote{A trivial RLWE ciphertext for plaintext $\mu(X)$ is a ciphertext of the form $(0, \ldots, 0, \mu(X))$.}

**Figure 2:** Binary representation of the constant coefficient from the polynomial plaintext encrypted in the accumulator during the Blind Rotate algorithm.

multiplied by successive powers of basis $B_{\psi}$ (up to $t_{\psi}$). The output is an LWE ciphertext encrypting the same message as the input, with respect to the original secret key.

**Programmatic Bootstrapping.** The programmatic bootstrapping algorithm implemented in CONCRETE takes on input an LWE ciphertext $c_{\text{in}}$ encrypting a plaintext $\mu_{\text{in}}$ under the key $s_{\text{in}} \in \mathbb{B}^n$, and a bootstrapping key; i.e., a list of $n$ RGSW ciphertexts, each one encrypting a bit of the secret key $s_{\text{in}}$ used to encrypt $c_{\text{in}}$. It outputs an LWE ciphertext $c_{\text{out}}$ encrypting the plaintext $\mu_{\text{out}}$ under the key $s_{\text{out}} \in \mathbb{B}^{n_\text{out}}$.

As depicted in Figure 3, this procedure is composed of three steps:

1. a modulus switching (to be compatible with $N$) wherein the input ciphertext in $(\mathbb{Z}_q)^{n+1}$ is converted into a ciphertext in $(\mathbb{Z}_qN)^{n+1}$;
2. a blind rotation of an RLWE ciphertext encrypting a redundant look-up table $t$ of size $N$ (which is rotated according to the output LWE ciphertext of Step 1);
3. a sample extraction of the constant coefficient of the RLWE.

It is essential to have at least one bit of padding left in the input LWE plaintext. This is a consequence of the negacyclic property: $X^{N+u} = -X^u$. Without padding the output is only guaranteed up to the sign.

**Look-up table.** There are multiple ways to represent a function $g : \mathbb{Z}_N \rightarrow \mathbb{Z}_q$. A simple way is to use a look-up table, which is a sort of dictionary containing pairs $(i, t[i])$, where $i$ is the input index and $t[i] = g(i)$. In our implementation, the value $t[i]$ is obtained by performing Encode’ $\circ f \circ$ Decode($i$). Observe that the encoding function might not be the one corresponding to Decode (i.e., Encode’ $\neq$ Encode); in fact, the bootstrapping enables to change encoding. These values are used to program the test polynomial
as \( T(X) = t_0 + \cdots + t_{N-1} X^{N-1} \) with \( t_i = t[i] \), and store it in an RLWE ciphertext. It is easy to see that the constant coefficient of \( T(X) \cdot X^{-i} \) is \( t[i] \), that is, the right output in the look-up table.

Note that if the goal of the bootstrapping is simply to reduce the noise, function \( f \) is the identity function.

Computing a Multiplication. Interestingly, a multiplication can be computed using two PBS operations. This follows from the observation that, in \( \mathbb{R} \), \( x \cdot y = (x+y)^2/4 - (x-y)^2/4 \). Over encrypted data, this amounts to one homomorphic addition and one homomorphic subtraction to get the evaluation of \( z = \frac{x+y}{2} \) and \( z = \frac{x-y}{2} \), and finally one homomorphic subtraction to get \( x \cdot y \).

3 CONCRETE IMPLEMENTATION

The CONCRETE\(^2\) library is built as a stack of layers (Fig. 4) from the closest to the hardware to the most abstracted and easy-to-use (Crypto API). In between, the API called Core API proposes an hardware generic API. At the moment, Core API only provides access to the CPU-based code version, but a GPU version (following the same API) is in preparation. On top of that, the Core API is wrapped in a more user-friendly API called Crypto API where every cryptographic object is represented as a structure. These structures also contain some metadata as the noise distribution, the number of padding bits or the decoding parameters. They are mainly used to ensure the correctness of the computation. The noise is monitored by a dedicated module abbreviated as NPE, for Noise Propagation Estimator.

The value of \( q \) is not fixed so that all functions are currently defined for either \( 2^{32} \) or \( 2^{64} \). Note that having functions for \( q = 2^{128} \) would be easy.

![Figure 3: Representation of the PBS steps as a sequence of bits. Each rectangle shows the noisy plaintext encrypted by an LWE ciphertext but the third which is the plaintext in the constant coefficient of an RLWE ciphertext.](image)

![Figure 4: Overview of the CONCRETE library architecture. Dashed boxes refer to upcoming features.](image)

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2The CONCRETE library is released under the AGPLv3 license.
which do not require any code modification by the end-user. For

The Crypto API

Core API

13 Core API wraps Core API in simple-to-use structures. Typically, metadata are directly included and automatically updated as the Core API homomorphic computation progresses. Code example 1 shows how to pick cryptographic parameters and generate secret keys (Lines 16 and 17), how to define an input encoder (Line 13), how to encode and encrypt a message (Line 23), how to generate a bootstrapping key (Line 20), how to define an output encoder (Line 30), how to homomorphically compute $e \rightarrow \max(0, \text{weight} \times c + \text{bias})$ with a PBS (Lines 36 and 37), and finally how to decrypt (Line 40).

Code example 2 shows how to pick cryptographic parameters and how to generate an RLWE secret key (Lines 2 and 3), how to define an input encoder (Line 6), how to encode and encrypt packed messages in RLWE ciphertexts (Lines 7 to 10), how to add two ciphertexts with padding (Line 13), to multiply by constants with padding (Line 14), how to extract an LWE from an RLWE (Line 16), and finally how to decrypt (Line 18).

The Crypto API introduces the following structures: Encoder, Plaintext, LWESecretKey, RLWESecretKey, LWEBSK, LWESKS, LWE, VectorLWE, VectorRLWE, LWEEParams, RLWEEParams.

3.1 Core API

The Core API is designed as an abstraction of the hardware dedicated code. Then depending on the underlying hardware actually present on the running machine, the API offers multiple choices which do not require any code modification by the end-user. For instance, the Core API offers some SIMD optimizations if supported by the CPU, or even running on a GPU if possible. Note that the latter is currently a mock-up and not open-sourced yet. Despite this convenient abstraction, the API aims to be as low-level as possible in order to yield better performance.

The API is divided into two main modules: (i) math containing non-cryptographic operations such as adding, subtracting or multiplying (unsigned) integers or polynomials, and (ii) crypto containing cryptographic-related operations such as encryption and decryption functions or bootstrapping and key switching functions.

Slices are used to represent messages, plaintexts, ciphertexts and keys. In Rust, a slice is a contiguous sequence of elements but at the difference of an array, its size is not known at compile time. A slice containing cryptographic-related operations such as encryption and decryption functions can compute a key switch for each of the ciphertexts in the slice.

Secret keys. The LWE or RLWE secret keys are all sampled from a uniform binary distribution. The keys are stored in the same way as ciphertexts: in slices of unsigned 32-bit/64-bit integers. In Core API, functions are able to work on a collection of ciphertexts. This is modeled as a single slice containing several concatenated LWE ciphertexts. For instance, the key switch function can compute a key switch for each of the ciphertexts in the slice.

Random source. Uniform random integers are generated with OpenSSL. Discretized Gaussian integers are obtained with a Box–Muller algorithm using OpenSSL uniform random integers.

Benchmarks. Tables 1 and 2 show the time needed to compute a PBS and a key switch for both $q = 32$ and $q = 64$. Benchmarks are obtained using Criterion, a Rust micro-benchmarking library. Each measurement is the mean duration of 500 iterations of the operation performed on a single thread.

Benchmarks were made on a personal computer with a 2.6 GHz 6-Core Intel® Core™ i7 processor.

Table 1: PBS in milliseconds from CPU Core API with $k = 1, B = 2^f$ and $f = 3$.

<table>
<thead>
<tr>
<th># bits</th>
<th>$N = 1024$</th>
<th>$N = 2048$</th>
<th>$N = 4096$</th>
</tr>
</thead>
<tbody>
<tr>
<td>$n = 630$</td>
<td>15.49 18.08 33.28 39.54</td>
<td>73.22 84.01</td>
<td></td>
</tr>
<tr>
<td>$n = 800$</td>
<td>19.23 22.98 42.33 50.53</td>
<td>93.12 107.3</td>
<td></td>
</tr>
<tr>
<td>$n = 1024$</td>
<td>24.54 29.16 54.14 64.18</td>
<td>117.9 135.2</td>
<td></td>
</tr>
</tbody>
</table>

Table 2: LWE key switching (in milliseconds) from CPU Core API with $B_s = 4$ and $\ell_s = 8$.

<table>
<thead>
<tr>
<th># bits</th>
<th>$n_{out} = 630$</th>
<th>$n_{out} = 800$</th>
<th>$n_{out} = 1024$</th>
</tr>
</thead>
<tbody>
<tr>
<td>$n_{in} = 1024$</td>
<td>0.9912 2.292</td>
<td>1.171 2.872</td>
<td>1.518 3.652</td>
</tr>
<tr>
<td>$n_{in} = 2048$</td>
<td>2.130 4.661</td>
<td>2.495 5.877</td>
<td>3.200 7.099</td>
</tr>
<tr>
<td>$n_{in} = 4096$</td>
<td>4.413 9.476</td>
<td>5.134 11.89</td>
<td>6.592 15.39</td>
</tr>
</tbody>
</table>
with the help of the

Table 4 gives the cost of a PBS and a key switch using the

Table 3:

vides an automatic generation for the look-up table associated to

multiplication using two PBS’s. The same setting as above is used.

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addition of two messages may result in a carry, which is translated

safety guard to ensure the computation correctness. For instance, an

most significant bits of the plaintext. Padding bits can be seen as a

REFERENCES

Code Example 2: A piece of code using CONCRETE’s Crypto API to encrypt
packed messages with RLWE and to compute constant multiplication and
homomorphic addition before extracting one slot as an LWE ciphertext.

Encoders and ciphers. An encoder is a structure containing the bounds of the real interval, the number of bits of precision, the number of padding bits and the type of encoding. The ciphertext structures VectorLWE and VectorRLWE defined in the Crypto API are able to store one or more ciphers of the same type (namely LWE or RLWE). It also contains the required metadata to track the noise, the encoding, and the number of bits of message guaranteed to be correct all along computations. The noise evaluation is realized with the help of the NPE module. In the case where the noise bits are devouting the bits of the message, a message informs the user about the intact number of bits and the number of bits of message is updated.

To encrypt, a call to the function LWE::encode_encrypt for instance, allows one to obtain an LWE ciphertext from a message, a secret key and an encoder. Note that the same function is present in modules VectorLWE and VectorRLWE.

Padding bits. Some homomorphic functions require some bits of padding in order to avoid the reduction modulo \( q \) and loose some most significant bits of the plaintext. Padding bits can be seen as a safety guard to ensure the computation correctness. For instance, an addition of two messages may result in a carry, which is translated into one padding bit. In the same vein, by default the PBS uses one padding bit and is able to add many of them in its output. In the Crypto API, homomorphic operations requiring padding bits are suffixed with _with_padding.

Automatic generation of the look-up table. The Crypto API provides an automatic generation for the look-up table associated to an arbitrary function \( f \) taking as input an f64 and outputting an f64, mainly called in the PBS. It takes as inputs the encoding of the inputs, the encoding of the outputs, and the function \( f \).

Benchmarks. Table 3 presents the time needed to compute a multiplication using two PBS’s. The same setting as above is used. Table 4 gives the cost of a PBS and a key switch using the Core API and the Crypto API for the parameter set given in [2].

Table 3:

Table 4:

<table>
<thead>
<tr>
<th># bits</th>
<th>CPU Core API</th>
<th>Crypto API</th>
</tr>
</thead>
<tbody>
<tr>
<td>PBS</td>
<td>15.49</td>
<td>18.08</td>
</tr>
<tr>
<td>Key Switch</td>
<td>0.9912</td>
<td>2.292</td>
</tr>
</tbody>
</table>

3.3 Planned Demo

During the demo, we plan to quickly give an overview of CONCRETE, and then to run live tests for basic operations (both exact and approximate) and for neural network inference.

4 FUTURE WORK

The CONCRETE library offers an efficient and user-friendly interface to homomorphically compute over ciphers. There is a variety of ways to use encoding such as representing approximation of real numbers. Thanks to the programmable bootstrapping, it is possible to compute non-linear functions in addition to homomorphic additions and multiplications by scalars.

We plan to add other homomorphic operations and features in the next versions of the library. In parallel, we are implementing the GPU version of the Core API level and building an automatic API dealing with padding and noise automatically on its own. Future works also include some studies about enhancing the FFT performance and further support of SIMD operations. Finally, we want to work on the parallelization of the existing code.

REFERENCES


